

FORM PTO 1449 (modified)				ATTY DOCKET NO. NL020321US		APPLICATION NO. 10/511,512	
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE LIST OF REFERENCES CITED BY APPLICANT(S) (Use several sheets if necessary)				APPLICANT Jeroen Anton Johan Leijten			
				FILING DATE October 14, 2004		GROUP 2183	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
/G.G./		5,274,831	12/28/1993	Katsuta			
/G.G./		5,452,401	9/19/1995	Lin			
/G.G./		5,832,202	11/3/1998	Slavenburg, et al.			
/G.G./		5,909,565	6/1/1999	Morikawa et al.			
/G.G./		6,317,820 B1	11/13/2001	Shiell et al.			
/G.G./		2001/049781 A1	12/6/2001	Miyake et al.			
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO/ OR ABSTRACT
/G.G./		100 08 008 C1	8/23/2001	DE			w/ machine Engl. translation
/G.G./		8-305568 A	11/22/1996	JP			w/ Engl. abst., see ctrpart US 5,909,565
/G.G./		2001-243066 A	9/7/2001	JP			w/ Engl. abst., see ctrpart US 2001/049781
/G.G./		00/26772 A1	5/11/2000	WO			English original
OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)							
/G.G./		Wu, "Power Analysis of Gated Pipeline Registers", ASIC/SOC Conf., IEEE, pgs. 281-85 (9/1999)					
/G.G./		Moon, "A 32-bit RISC Microprocessor With DSP Functionally: Rapid Prototyping", IEICE Trans. on Funds. of Electronics, Communications and Computer Sciences, vol. E84-A, no. 5, pgs. 1339-47 (5/2001)					
EXAMINER /George Giroux/				DATE CONSIDERED 06/27/2010			

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.